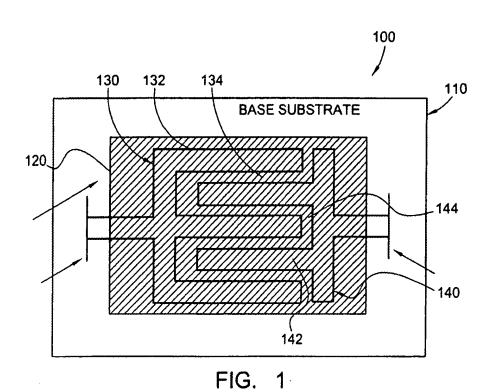
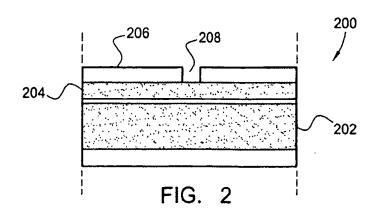
Inventor: TONCICH, STANLEY S.

Fighg Date: 12/31/2003

Appl. No.: 10/750,304 Atty. Doc. No.: UD1 00001

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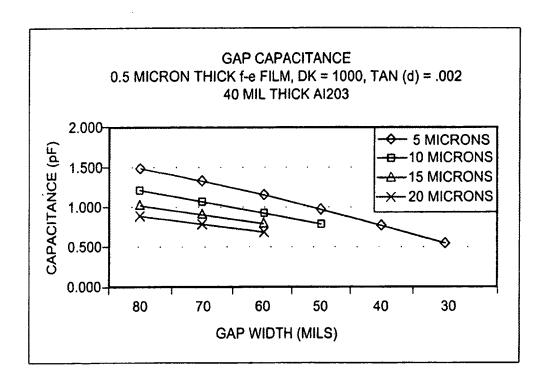


Inventor: TONCICH, STANLEY S.

Filing Date: 12/31/2003

Appl. No.: 10/750,304 Atty. Doc. No.: UD1 00001

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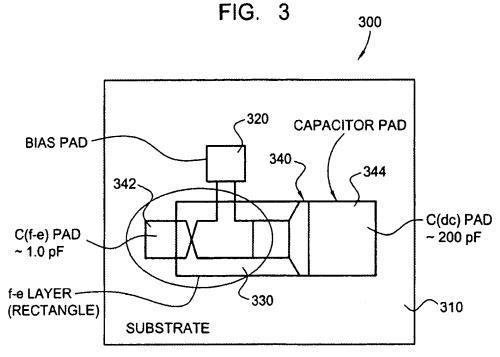
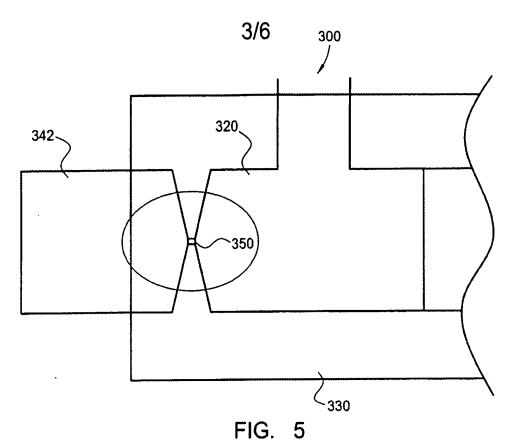


FIG. 4

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VDC 402 404 408 408

FIG. 6

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								T	
KWC	NRL	NRL	NRL	KWC	KWC	KWC	fo	TUN-	COM-
	ŀ	CAPA-	Q	CAP	Q	Q		ING	MENTS
		CITOR							
SAM-	SAM-	(pF)	0)	(pF)	(0	(40	(MHz)	(MHz/	
PLE #	PLE#		VDC)	,, ,	VDC)	VDC)		V)	
01	NRL#	1.012	10.1	0.99	190		1622	~ 1.0	
	1-A								
02	NRL#	1.012	10.1	0.96	183		1605	~ 1.0	
	1-A								
01	NRL#	1.004	10.1	0.90	101	123	1610	~ 1.0	
	3-A					_			
02	NRL#	0.76	10.2	0.75	200		1900	~ 0.5	
	2-A								
06	NRL#	0.76	10.2	0.72	205			~0.5	DAMAGED,
]	2-A								HIGH V
05	NRL#	0.76	10.2	0.71	196	200	1912	~ 0.5	
	2-A								

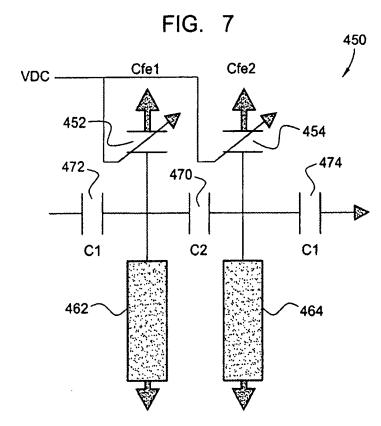


FIG. 8

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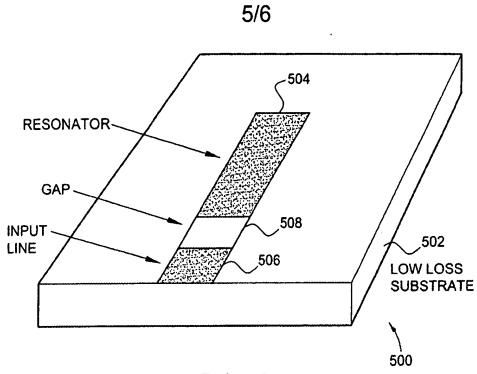


FIG. 9

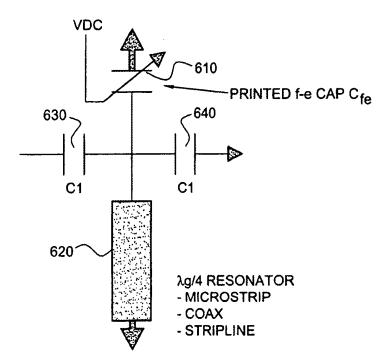


FIG. 10a

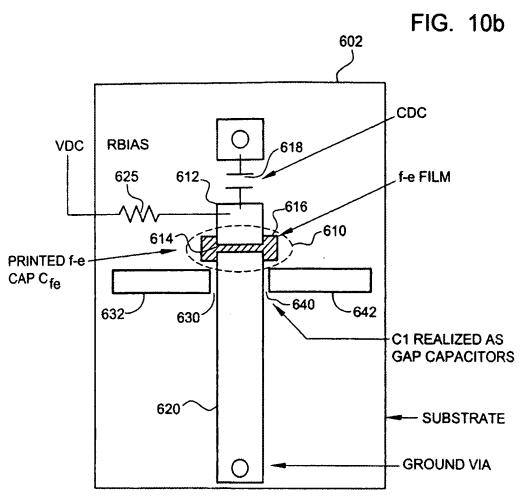
Mentor: TONCICH, STANLEY S.

Ming Date: 12/31/2003

REPLACEMENT SHEEET

Appl. No.: 10/750.304 Atty. Doc. No.: UD1 00001

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PLANAR REALIZATION OF SINGLE RESONATOR BPF. CAN BE REALIZED WITHOUT VIA'S USING GROUND PLANES & A WILTRON TEST FIXTURE.

EXAMPLE OPERATING PARAMETERS

Temperature (degrees C)

Example 1: -50 to 100

Quality Factor (Q)

Example 1: > 80

Example 2: > 180

Capacitance (pF)

Example 1: 0.3 to 3.0

Example 2: 0.5 to 1.0

Example 3: 0.8 to 1.5

Frequency (GHz)

Example 1: 0.25 to 7.0

Example 2: 0.8 to 7.0

Example 3: 0.25 to 2.5

Example 4: 0.8 to 2.5